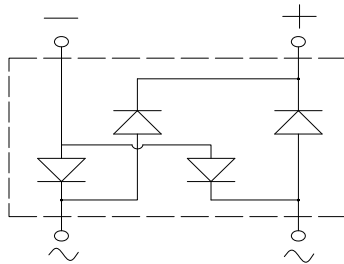
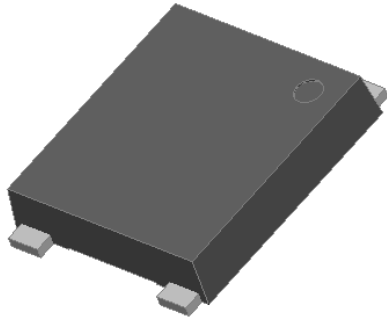


Low VF Bridge Rectifiers



Features

- UL recognition, file #E313149
- Glass passivated chip junction
- Ideal for automated placement
- High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

Typical Applications

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

Mechanical Data

- **Package:** YBS
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, Halogen-free
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** As marked on body

■Maximum Ratings (T_a=25 Unless otherwise specified

PARAMETER	SYMBOL	UNIT	YBSL3006A
Device marking code			YBSL3006A
MM @60Hz sine wave, R-load, T _c =125	I		

	o	A	3.0
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, T _j =25	I _{FSM}	A	120
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, T _j =25			240
Current squared time @1ms≤t<8.3ms T _j =25 Rating of per diode	I ² t	A ² s	59.8
Storage temperature	T _{stg}		-55 ~ +150
Junction temperature	T _j		-55 ~ +150

■Electrical Characteristics T_a=25 Unless otherwise specified

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	Min	Typ	Max
Instantaneous forward voltage drop per diode	V _F	V				

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YBSL3006A

■ Thermal Characteristics $T_a=25$ Unless otherwise specified

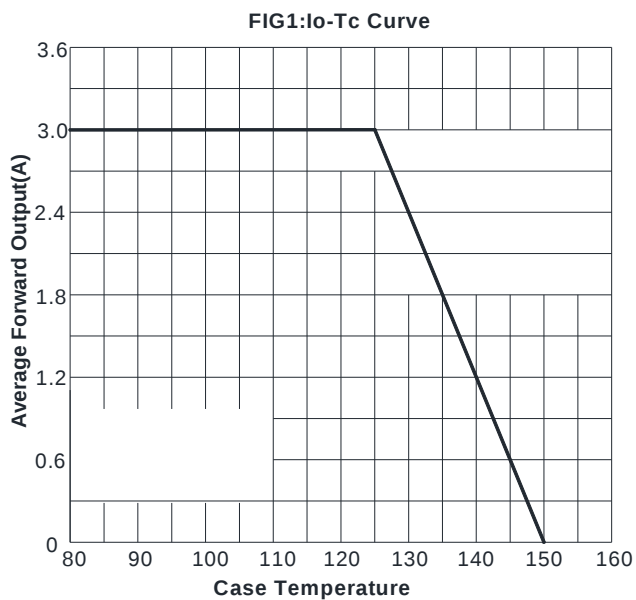
PARAMETER		SYMBOL	UNIT	YBSL3006A
Typical Thermal Resistance	Between Junction and Ambient	$R_{\theta JA}$	/W	55.0
	Between Junction and Lead	$R_{\theta JL}$		15.0
	Between Junction and Case	$R_{\theta JC}$		8.0

Note: Device mounted on P.C.B with 35mm*25mm*1.7mm.

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YBSL3006A	F1	Approximate 0.220	3000	/	42000	13" reel

■ Characteristics(Typical)





YBSL3006A

■ Outline Dimensions

D	7.90	8.60
7.90	8.0	

■ Suggested pad layout

Dim	Min
P1	9.15
P2	7.10
Q1	1.80
Q2	2.00



Disclaimer

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