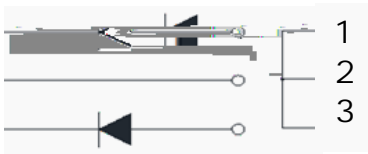


als: Tin plated leads, solderable per J-STD-001  
 JESD22-B102  
 : As marked



**Maximum Ratings (T<sub>j</sub>=25 °C Unless otherwise specified)**

PARAMETER	SYMBOL	UNIT	MUR2060PT
Device marking code			MUR2060PT
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	V	600
Average Rectified Output Current @60Hz sine wave, R-load, T <sub>c</sub> (FIG.1)	I <sub>o</sub>	A	20
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, T <sub>j</sub> =25 °C	I <sub>FSM</sub>	A	120
Current Squared Time @1ms t 8.3ms T <sub>j</sub> =25 °C	I <sup>2</sup> t	A <sup>2</sup> s	60
Single Pulse Avalanche Energy @ Tp=40uS, T <sub>j</sub> =25 °C, L=15mH	EAS	mJ	24
Storage Temperature	T <sub>stg</sub>	-	-55 ~ +175
Junction Temperature	T <sub>j</sub>	-	-55 ~ +175
Typical Junction capacitance @4V,1MHz	C <sub>j</sub>	pF	40



# MUR2060PT

## Electrical Characteristics

PARAMETER	UNIT	TEST CONDITIONS	Min	Typ	Max
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