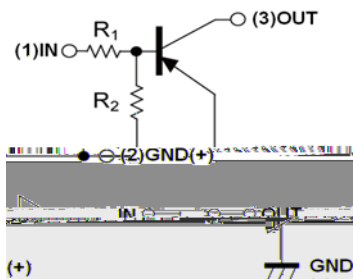




Moisture sensitivity level 1
Halogen free and RoHS compliant
Surface mount package ideally suited for automatic insertion



Signal amplification
Switching circuit

SOT-23
Tin plated leads, solderable per J-STD-002 and JESD22-B102

($T_a=25$ Unless otherwise specified)

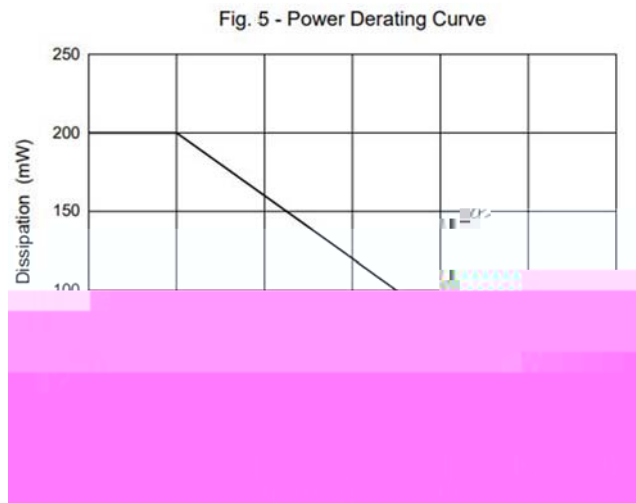
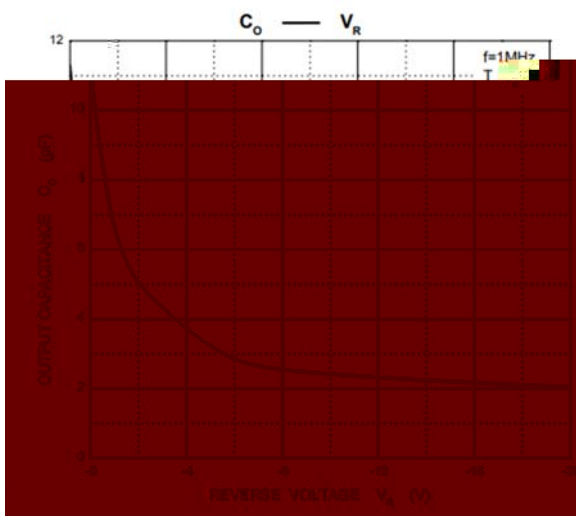
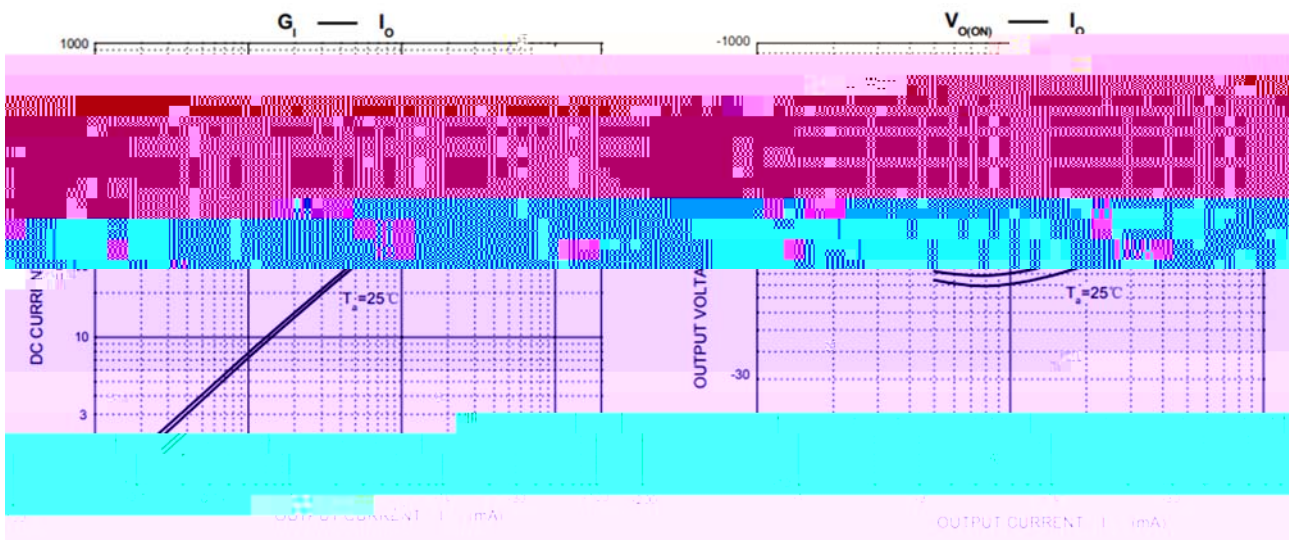
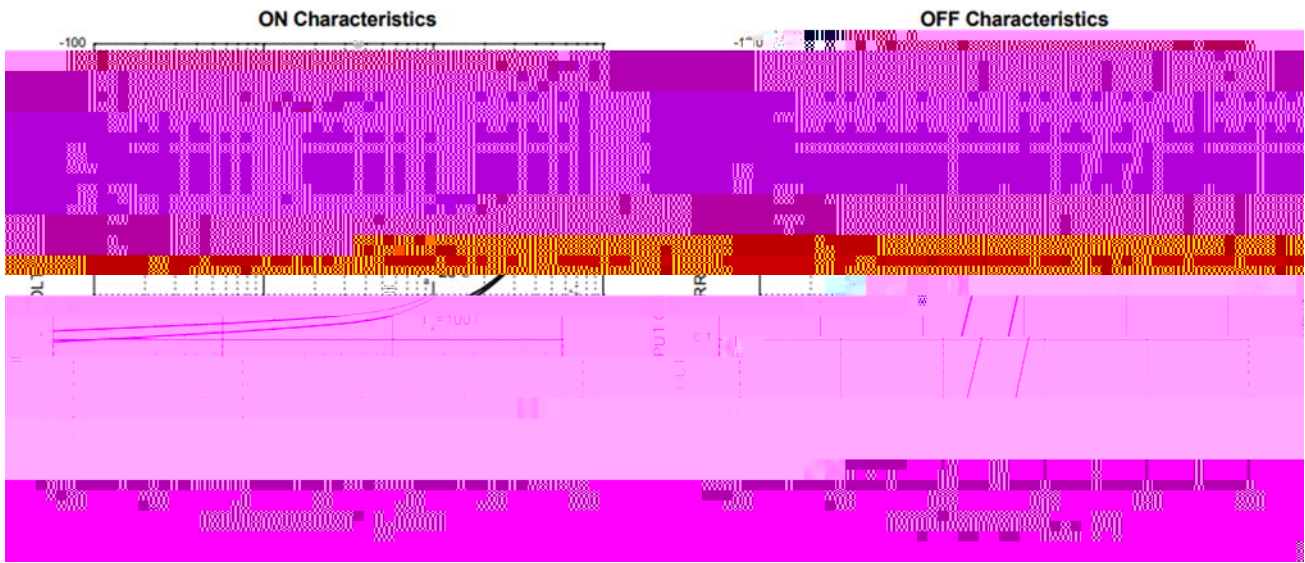
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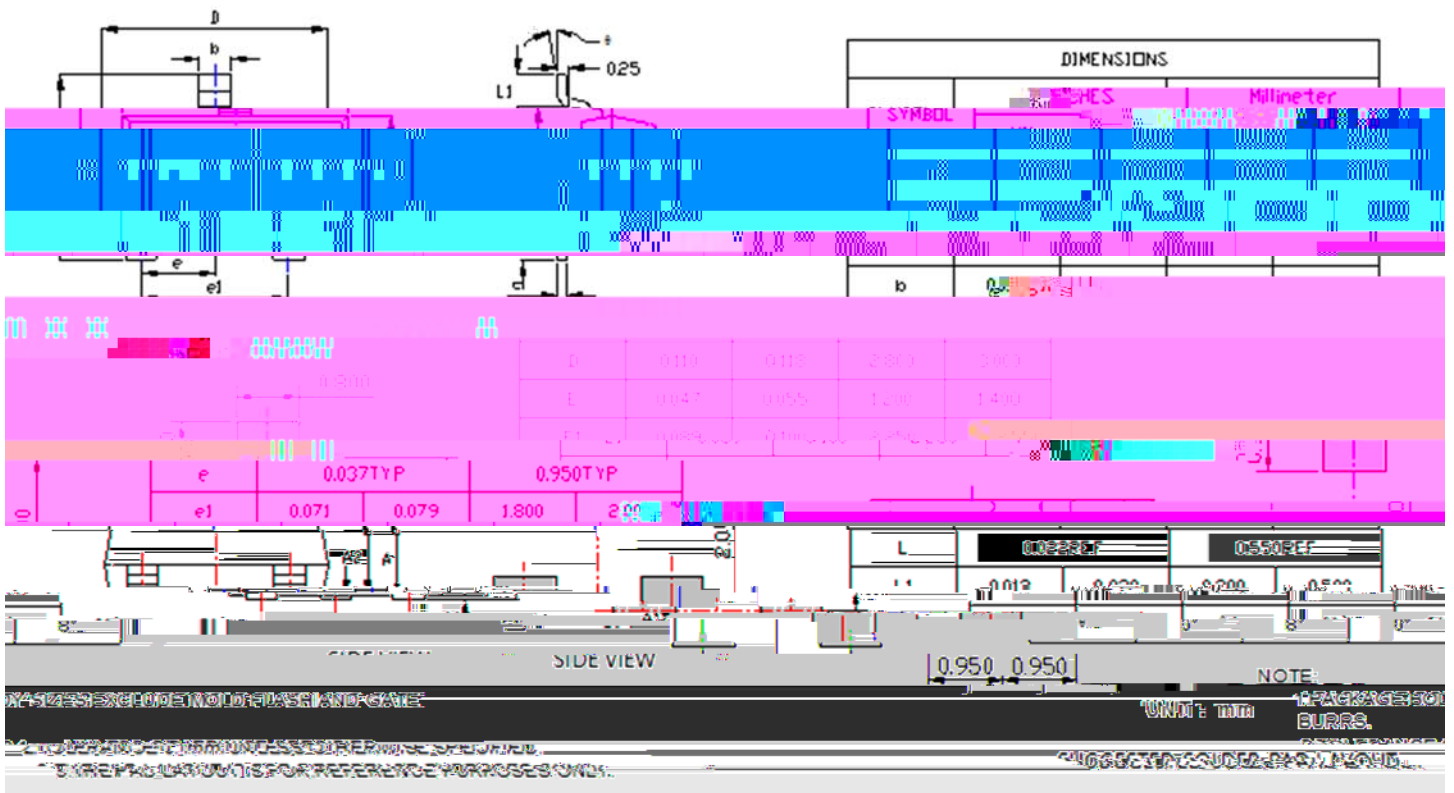
				-55 to +150
Storage temperature	T_{STG}			-55 to +150



($T_a=25$ Unless otherwise specified)

Input voltage	$V_{I(off)}$	V	$V_{CC}=-5V, I_O=-100\mu A$	-0.5		
	$V_{I(on)}$	V	$V_O=-0.3V, I_O=-20mA$			-3.0
Output voltage	$V_{O(on)}$	V	$I_O / I_I = -10mA / -0.5 mA$			-0.3
Input current	I_I	mA	$V_I = -5V$			-1.8
Output current	$I_{O(off)}$	μA	$V_{CC} = -50V, V_I = 0$			-0.5
DC current gain	G_I					





Note:

- All dimensions are in millimeters (mm) unless otherwise specified.

(所有尺寸均以毫米为单位，除非另有说明)

All dimensions shown are exclusive of burrs and gate residues. Burrs and gate vestiges shall not exceed 0.15 mm in maximum.

[所有尺寸均不包括毛刺和胶口残迹。毛刺与胶口残迹的最大尺寸不得超过0.15mm。尺寸b不包括单边最大0.100mm的中筋凸出部分]

7. Formed leads shall be planar with respect to one another within a maximum of 0.076 mm relative to the seating plane.

[成形的引脚应相互平行，并相对于最大0.076mm的平面。D和E是塑封体的外部极限尺寸；不包括包封溢料、内引线溢料、凸出部分以及胶体毛刺，但是包含了包封错位的最大尺寸]

★It is the key size.

[★标记为关键尺寸]



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