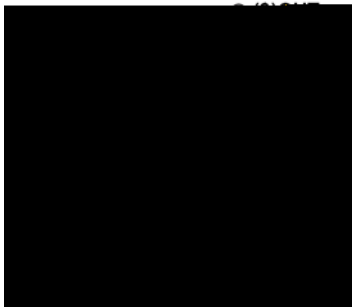
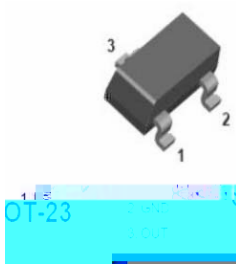


PNP Digital Transistors (Built-in Resistors)



Features

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-
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Application

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Mechanical data

- **Package** •
- **Terminals** •

Maximum Ratings

Item	Symbol	Unit	Conditions	Value
Input voltage	V_i	V		-4 \$ to Ž 1 \$
Output current	I_o	mA		-1 \$ \$
Power dissipation	P_D	mK		2 \$ \$
> unction temperature	T_j	°C	°C	É Í Ác [ÁÉF í €Á
Úc [!æ*^hc^ {] ^!æc~!^ÁÁ	$V_{úvóÁ}$	°C	°C	É Í Ác [ÁÉF í €Á

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RoHS
COMPLIANT

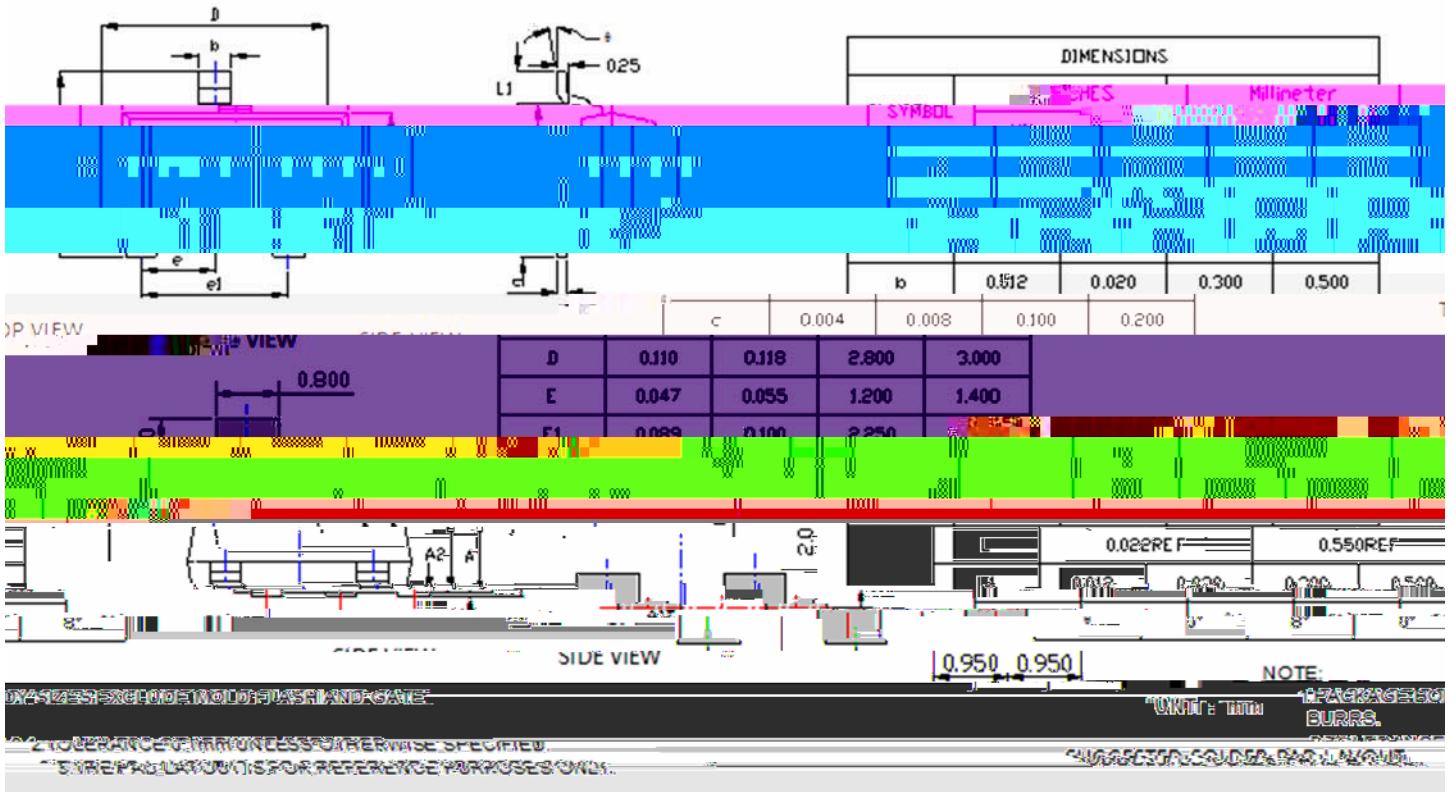
Á Á HÁDÁ Í Á

ÜÈÛÎFHÍÁ
Ü^çÈFÈF€JÉRæ}ÈGÎÁ

Yæ} * :@ [^ÁYæ} *bâ^ÁÒ|^&c! [] â&ÁV^&@} [[[* ^ÁÔ [ÈÈÁŠcâÈÁ , , , ÈGF^æ} *bâ^È& [{



Outline Dimensions



Note:

- 1. All dimensions are in millimeters (mm) unless otherwise specified.

All dimensions shown are exclusive of burrs and gate residues. Burrs and gate vestiges shall not exceed 0.15 mm in maximum.

所有尺寸均不包括毛刺及胶体。毛刺与胶体残留物最大不得超过0.15mm。

Dimensions do not include protrusions or mold flash, max. 0.100 mm, on outside.

尺寸不包括单边最大0.100 mm的单筋凸出部分。

Dimensions do not include protrusions or mold flash, max. 0.100 mm, on outside.

所有尺寸均不包括毛刺及胶体。毛刺与胶体残留物最大不得超过0.15mm。

Dimensions do not include protrusions or mold flash, max. 0.100 mm, on outside.

所有尺寸均不包括毛刺及胶体。毛刺与胶体残留物最大不得超过0.15mm。

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