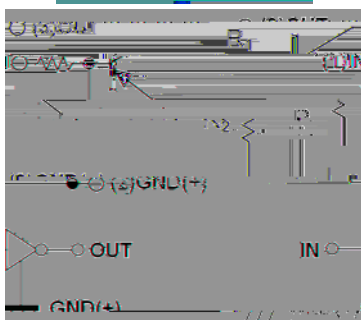




PNP Digital Transistors (Built-in Resistors)



Features

- Moisture sensitivity level 1
- Halogen free and RoHS compliant
- Surface mount package ideally suited for automatic insertion

Application

- Signal amplification
- Switching circuit

Mechanical data

- Package** SOT-23
- Terminals** Tin plated leads, solderable per J-STD-002 and JESD22-B102

■ Maximum Ratings ($T_a=25^\circ\text{C}$ Unless otherwise specified)

Item	Symbol	Unit	Conditions	Value
Device marking code				AB
Supply voltage	V_{CC}	V		-50
Input voltage	V_{IN}	V		-40 to +10
Output current	I_O	mA		-100
Power dissipation	P_D	mW		200
Junction temperature	T_J	$^\circ\text{C}$		-55 to +150
Storage temperature	T_{STG}	$^\circ\text{C}$		-55 to +150



DTA115ECA

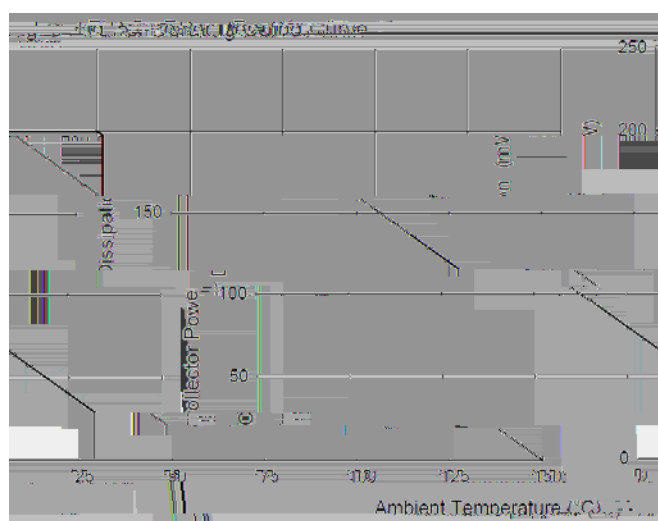
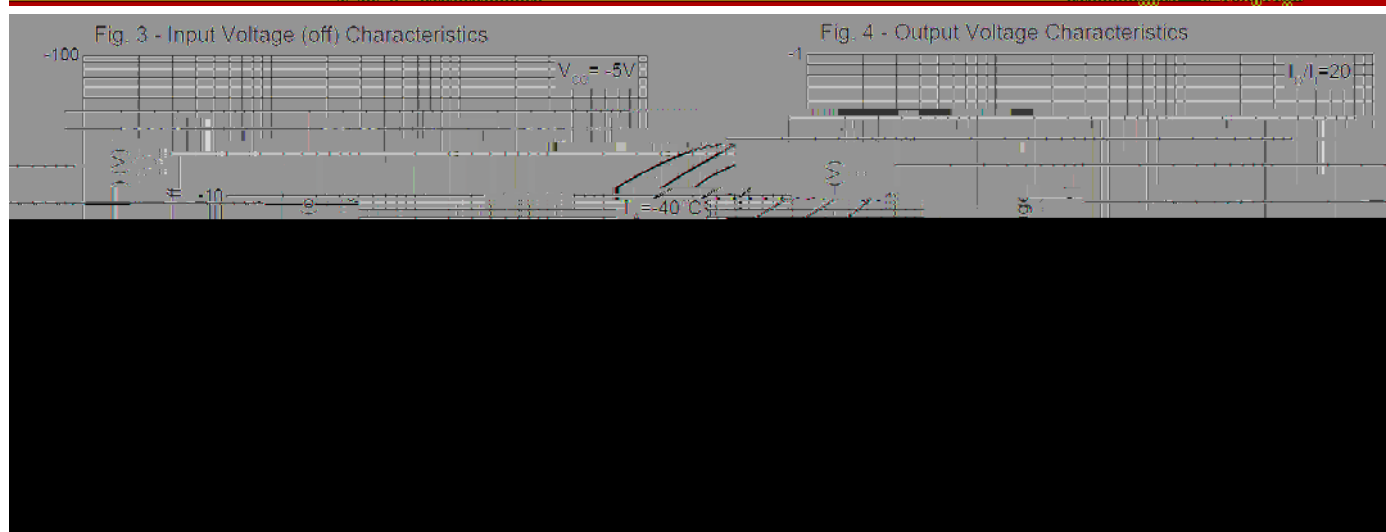
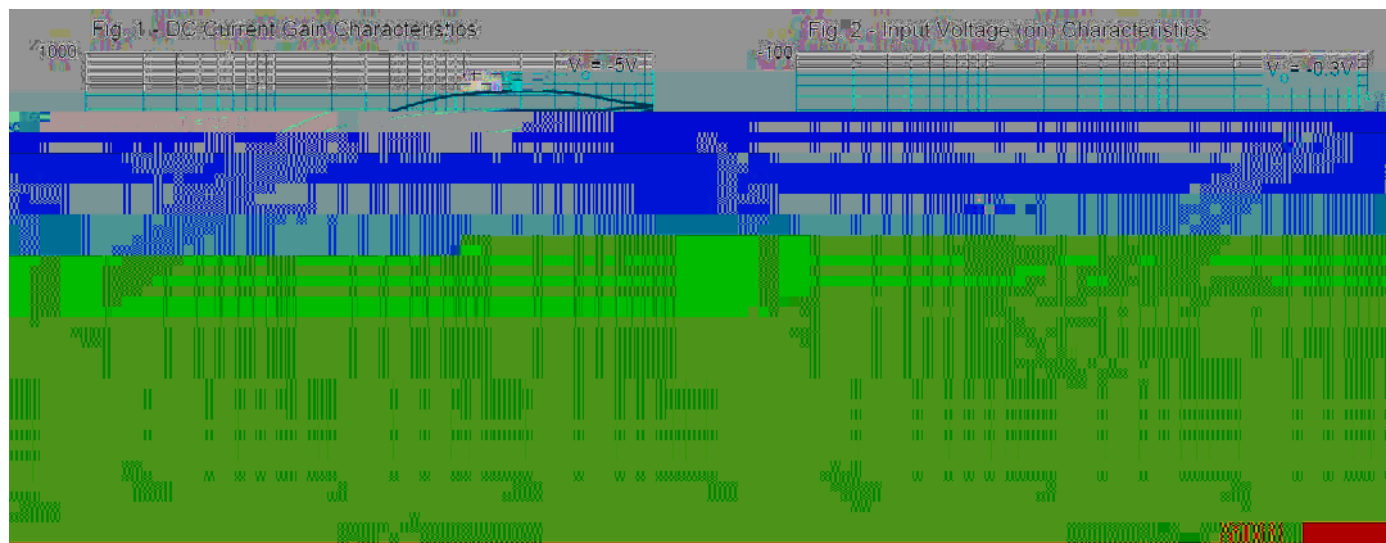
■ Electrical Characteristics (T_a=25 Unless otherwise specified)

Item	Symbol	Unit	Conditions	Min	Typ	Max
Input voltage	V _{I(off)}	V	V _{CC} =-5V, I _O =-100uA	-0.5		
	V _{I(on)}	V	V _O =-0.3V, I _O =-1mA			-3.0
Output voltage	V _{O(on)}	V	I _O / I _I = -5mA / -0.25mA			-0.3
Input current	I _I	mA	V _I =-5V			-0.15
Output current	I _{O(off)}	uA	V _{CC}			



DTA115ECA

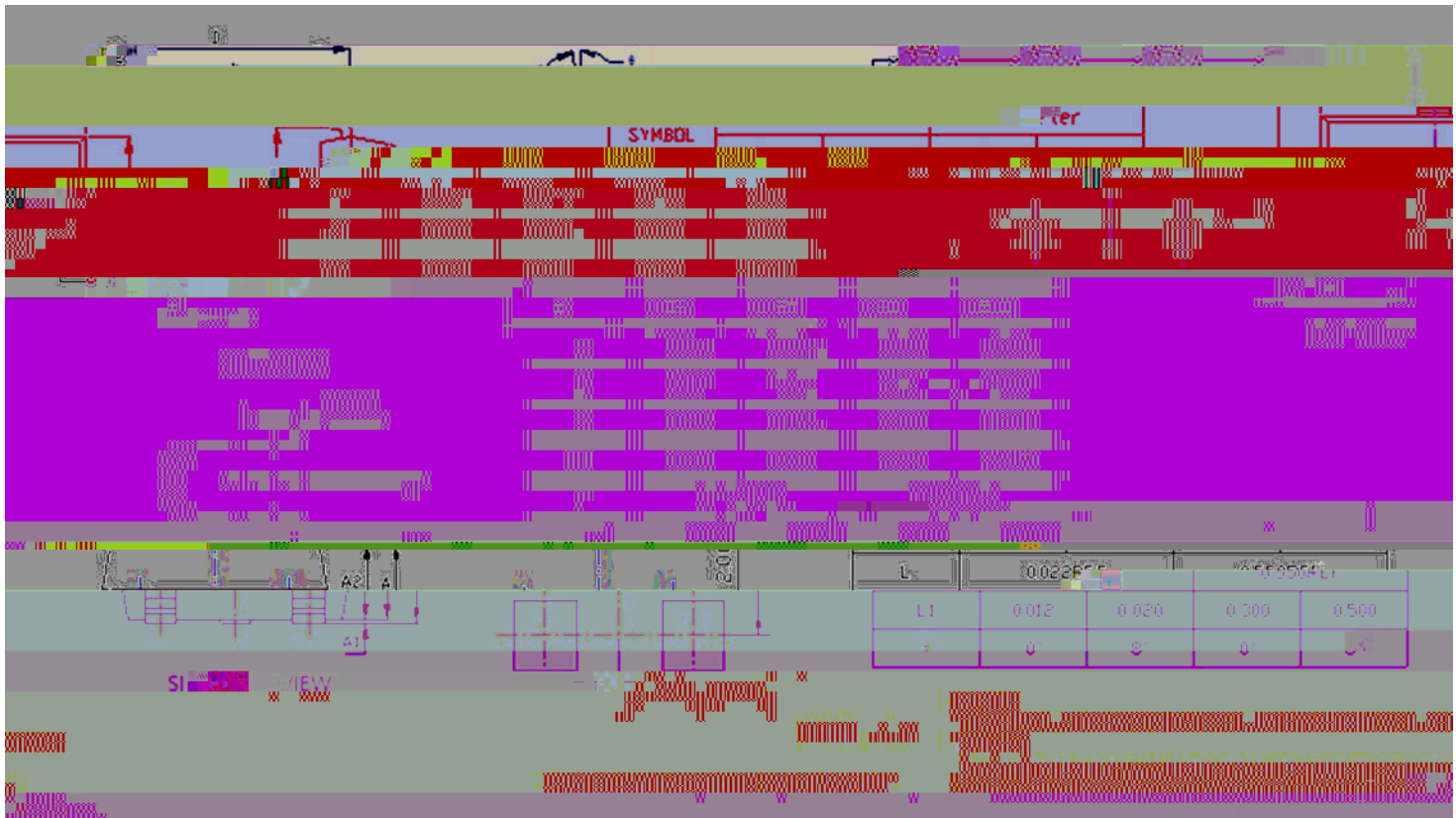
■Characteristics





DTA115ECA

■ Outline Dimensions



Note:

1. All dimensions are in millimeters (mm) unless otherwise specified.
[所有尺寸均以毫米为单位，除非另有说明]
2. General tolerances: $\pm 0.10\text{mm}$ unless otherwise specified.
[通用公差为 $\pm 0.10\text{mm}$ ，除非另有说明]
3. Dimensions and tolerances per ASME Y14.5M-2018.
[尺寸和公差遵循“ASME Y14.5M-2018”标准]
4. Lead length does not include mold flash, burr, protrusion, batch deviation, etc. and batch deviation should not exceed 0.15 mm in maximum.
[所有尺寸均不包括毛利和浇口残留。毛利与浇口残留的尺寸最大不得超过 0.15mm]
5. Dimension does not include barrel protrusion of max 0.100 mm per side.
[尺寸不包括最大为 0.100 mm 的引脚凸出部分]
6. Dimensions do not include the overall extreme outer dimensions of the mold compound. These exclude mold flash, lead flash, protrusions and burrs but include the maximum allowable mold mismatch.
[0和5是塑封体的外部极限尺寸，不包括包封毛利、引脚残溢料、凸出部分以及胶体毛利，但是包含了包封错位的最大尺寸]
7. Formed leads shall lie in one plane with respect to one another within a maximum of 0.1 mm relative to the seating plane.
[成型的管脚应为同一平面，共面性最大为 0.1mm]
8. ★ For the lead size: 0.40±0.05 mm for size 10.
[★ 标记为关键尺寸]



DTA115ECA

Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of t