



HS2MAQ

Surface Mount High Efficient Rectifier

Features

" /RZ SURILOH SDFNDJH
" ,GHDO IRUWDHXSOPDFHFPHQW
" *ODVV SDVVLYDWHG FKLS MXQFWLRQ
" +LJK IRUZDUG VXUJH FDSDELOLW\
" 6XSHU)DVW UHYHUVH UHFRYHU\ WLPH
" 0HHWV 06/ OHYHO SHU - 67' /) PD[LPXP
RI f &
" 3DUW QR ZLWKHDXQVL\$(3& 4 TXDOLILHG

Typical Applications

)RU XVH LQ KLJKFIDWHLXDOVLRQHRI SRZHU
VXSSOLHV LQYHUWHUV FRQYHUWHUV DQG
FRQVXPHU DXWRPRWLYH DQG WHOHFRPPXQ

Mechanical Data

" Package '2 \$& 60\$
0ROGLQJ FRPSR8/QG PHHWDPPDELOLW\
UDWLQJ 5R+6 FRPSOLDQW KDORJHQ IUHH
" Terminals 7LQ SODWHG OHDGV VROGHUDEOH S
- 67' DQG -(6' %
" Polarity: &DWKRGH OLVKBFQWKIRGH HQG

Maximum Ratings 7D - 8QOHVV RWKHUZLVH VSHFLILHG

PARAMETER	SYMBOL	UNIT	HS2MAQ
'HYLFH PDUNLQJ FRGH 0D[LPXP 5HSHWLWLYH SHDN UHYHUVH YROWDJH # +]			+DOI VLQH ZDYH F\FOH 7D '
	)60	\$	

6WRUDJH WHPSHUDWXUH

7_{VJ}

a

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	HS2MAQ
0D[LPXP LQVWDQW IRUZDUG YROWDJH	DQHRXV GURS SHU ⁹	GLRGH)0	\$	
0D[LPXP UHYHUVH UHFRYHU\ WLPH	7 ₅₅		\$ ⁵¹ \$ ⁵¹ \$ ⁵¹ V	
0D[LPXP '& UHYHUVH FXUUHQW DW UDWHG '& EORFNLQJ YROWDJH SHU GLRGH ⁹ 9 ₅₀		\$	7D 7D	
7\SLFDO MXQFWLRQ FDSDFLWSQFH 9 9 l& 0+]				



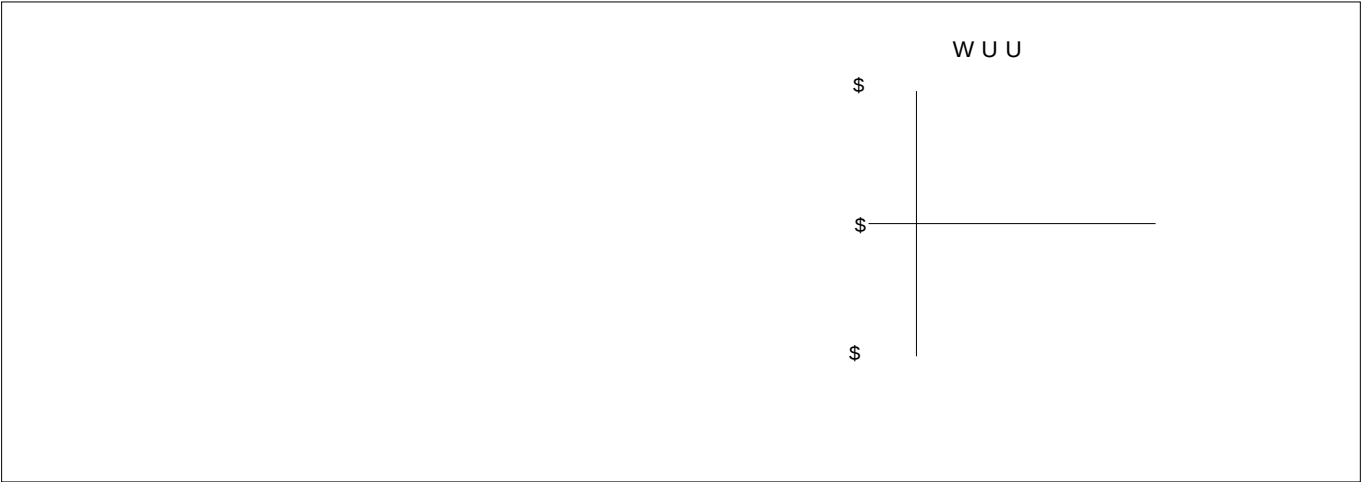
Thermal Characteristics 7D - 8QOHVV RWKHUZLVH VSHFLILHG

PARAMETER



HS2MAQ

Fig.5: Diagram of circuit and Testing wave form of reverse recovery time

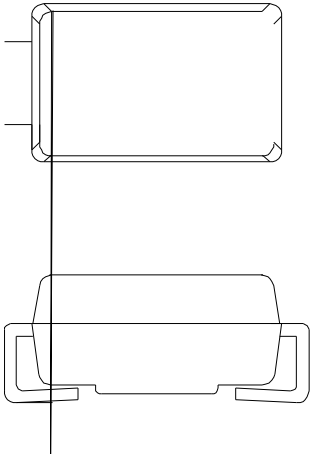


Ordering Information ([DP SOH

PREFERED P/N	PACKAGE CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
+ 6 0 \$4	)	\$ S S U R [L P D W H				' U H H O

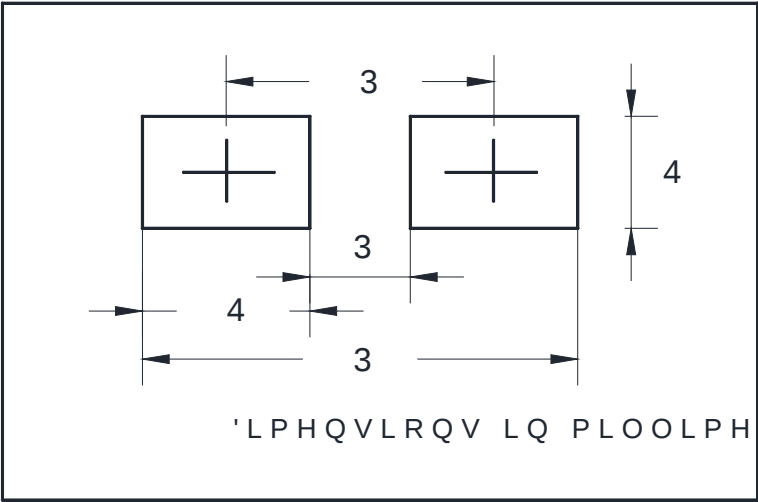


vOutline Dimensions



DO-214AC(SMA)		
' L P	0 L Q	0 D
\$		
%		
&		
'		
(		
)		
*		
+		
,		

■Suggested Pad Layout



DO-214AC(SMA)	
' L P	0 L O O L P H W H U V
3	
3	
3	
4	
4	

