

$V_{DS} = 40V$
 $V_{GS} = \pm 20V$
 $I_D = 7A$
 $I_{DM} = 96A$
 $E_{AS} = 6.25mJ$
 $P_D = 1.6W$
 $T_J, T_{STG} = -55 \sim +150^\circ C$

e cá rOLV M SFE t á
 H gá it O e sd Og fo Ol wo
 O gá O w t h n
 M st e Se sitivit Le e 1
 Epoxy Meets UL 94 V-0 Flammability Rating
 Halogen Free

Wireless charger
 Load switching
 Power management

($T_A=25$ unless otherwise noted)

Drain-source Voltage		V_{DS}	40	-40	V
Gate-source Voltage		V_{GS}	± 20	± 20	V
Drain Current	$T_A=25$	I_D	7	-5	A
	$T_A=100$		4	-3	
	$T_C=25$		24	-18	
	$T_C=100$		15	-11	
Pulsed Drain Current ^A		I_{DM}	96	-72	A
Avalanche energy ^B		EAS	6.25	6.25	mJ
Total Power Dissipation ^C	$T_A=25$	P_D	1.6	1.6	W
	$T_A=100$		0.6	0.6	
	$T_C=25$		18.9	19.5	
	$T_C=100$		7.5	7.8	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 +150	-55 +150	

Thermal Resistance Junction-to-Ambient	Steady-State	R_{JA}	60	75	60	75	/W
Thermal Resistance Junction-to-Case	Steady-State	R_{JC}	5.5	6.6	5.3	6.4	

(Example)

YJQ016NP04A	F1	Q016NP04A	5000	10000	100000	13" reel
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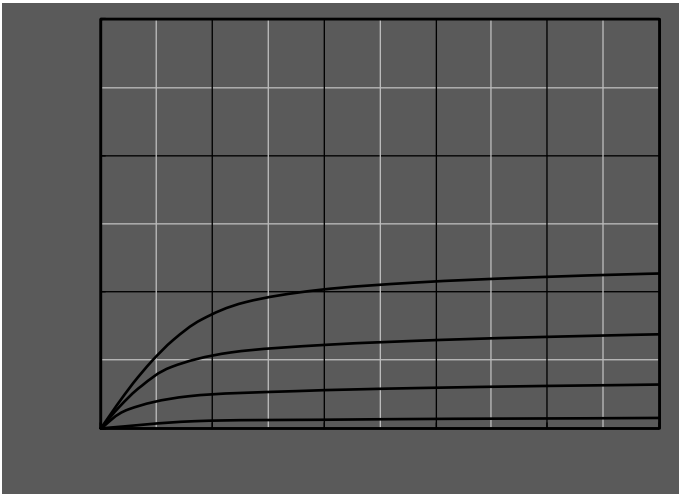
(T_J=25 unless otherwise noted)

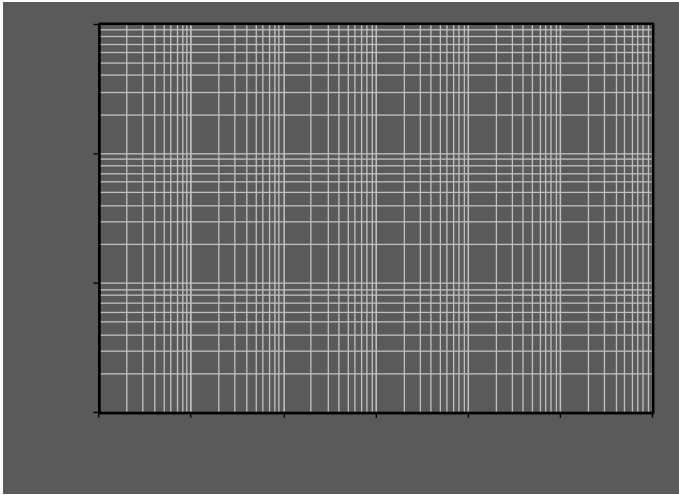
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(T_J=25 unless otherwise noted)

Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V	-	-	-1	μA
		V _{DS} =-40V, V _{GS} =0V, T _J =150	-	-	-100	





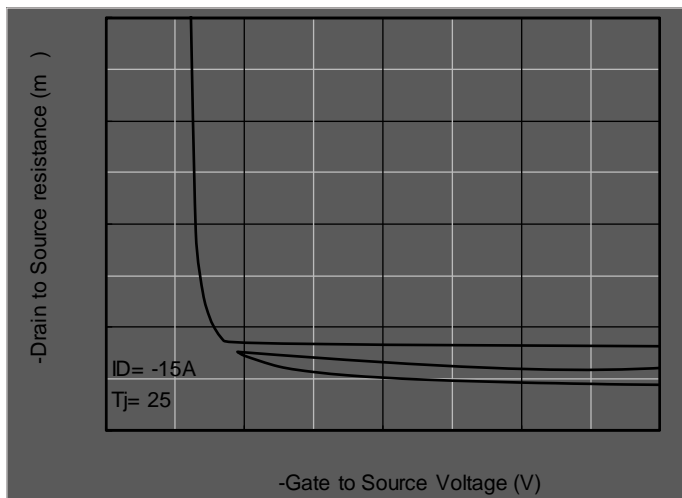


Figure 5. On-Resistance vs Gate to Source Voltage

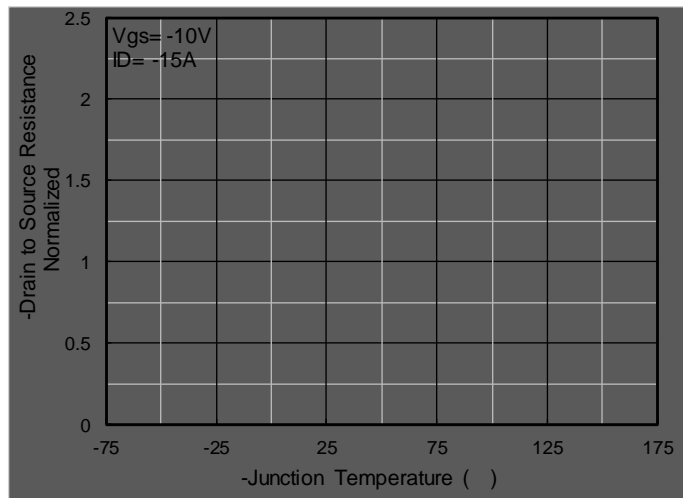
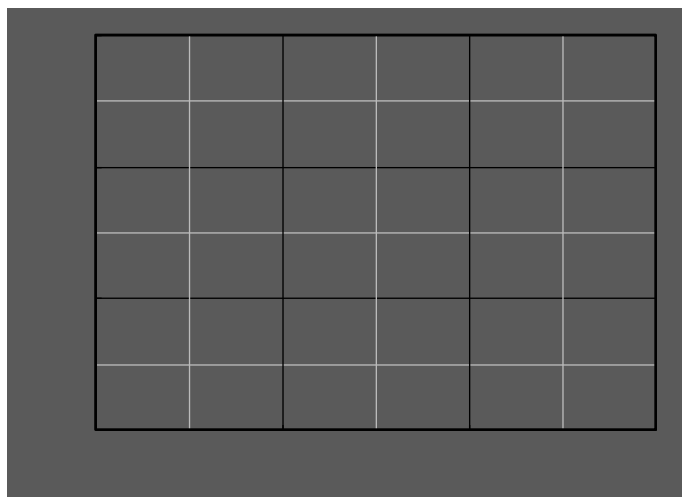
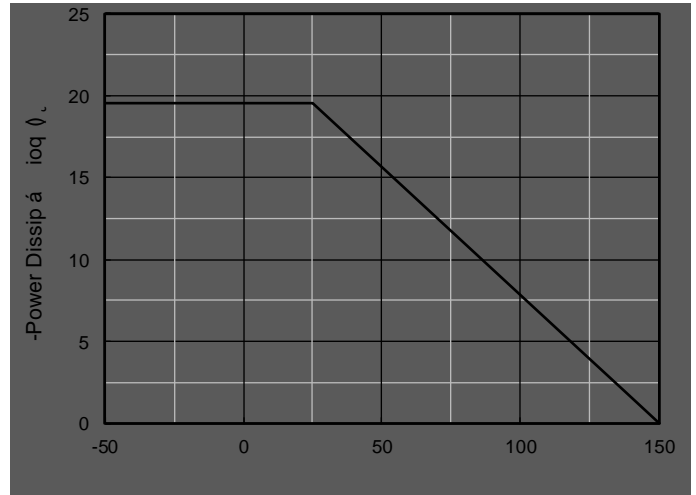
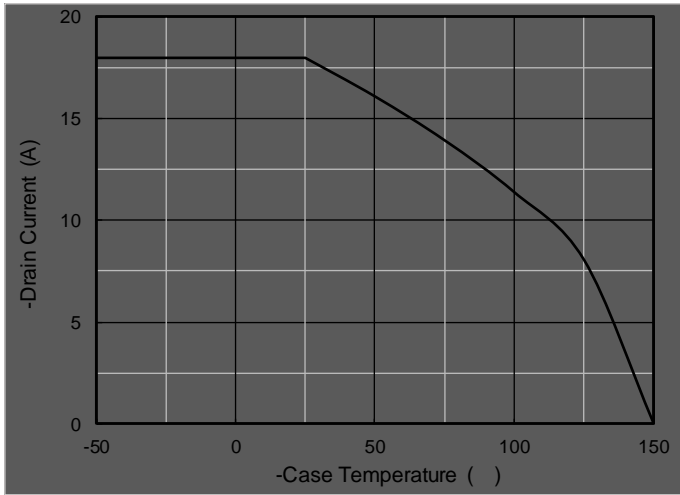


Figure 6. Normalized On-Resistance







Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.10\text{mm}$.
3. The pad layout is for reference purposes only.

Suggested Solder Pad Layout



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