



χ V_{DS}	100V
χ I_D	4.0A
χ $R_{DS(ON)}$ (at $V_{GS}=10V$)	110mohm
χ $R_{DS(ON)}$ (at $V_{GS}=4.5V$)	120mohm

- χ Trench Power MV MOSFET technology
- χ Excellent package for heat dissipation
- χ High density cell design for low $R_{DS(ON)}$
- χ Epoxy Meets UL 94 V-0 Flammability Rating
- χ Halogen Free

- χ DC-DC Converters
- χ Power management functions

Ω (T_A=25 unless otherwise noted)

Drain-source Voltage		V_{DS}	100	V
Gate-source Voltage		V_{GS}	020	V
Drain Current	T _A =25	I_D	4	A
	T _A =70		3.2	
Pulsed Drain Current ^A		I_{DM}	16	A
Total Power Dissipation @ T _A =25		P _D	2.5	W
Thermal Resistance Junction-to-Ambient ^B		R _{θJA}	50	/ W
Thermal Resistance Junction-to-Case		R _{θJC}	16	/ W
Junction and Storage Temperature Range		T _J , T _{STG}	-55 +150	

Ω (Example)

	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJM04N10A	F2	1004	2500	/	40000	136 reel
			2500	5000	25000	136 reel

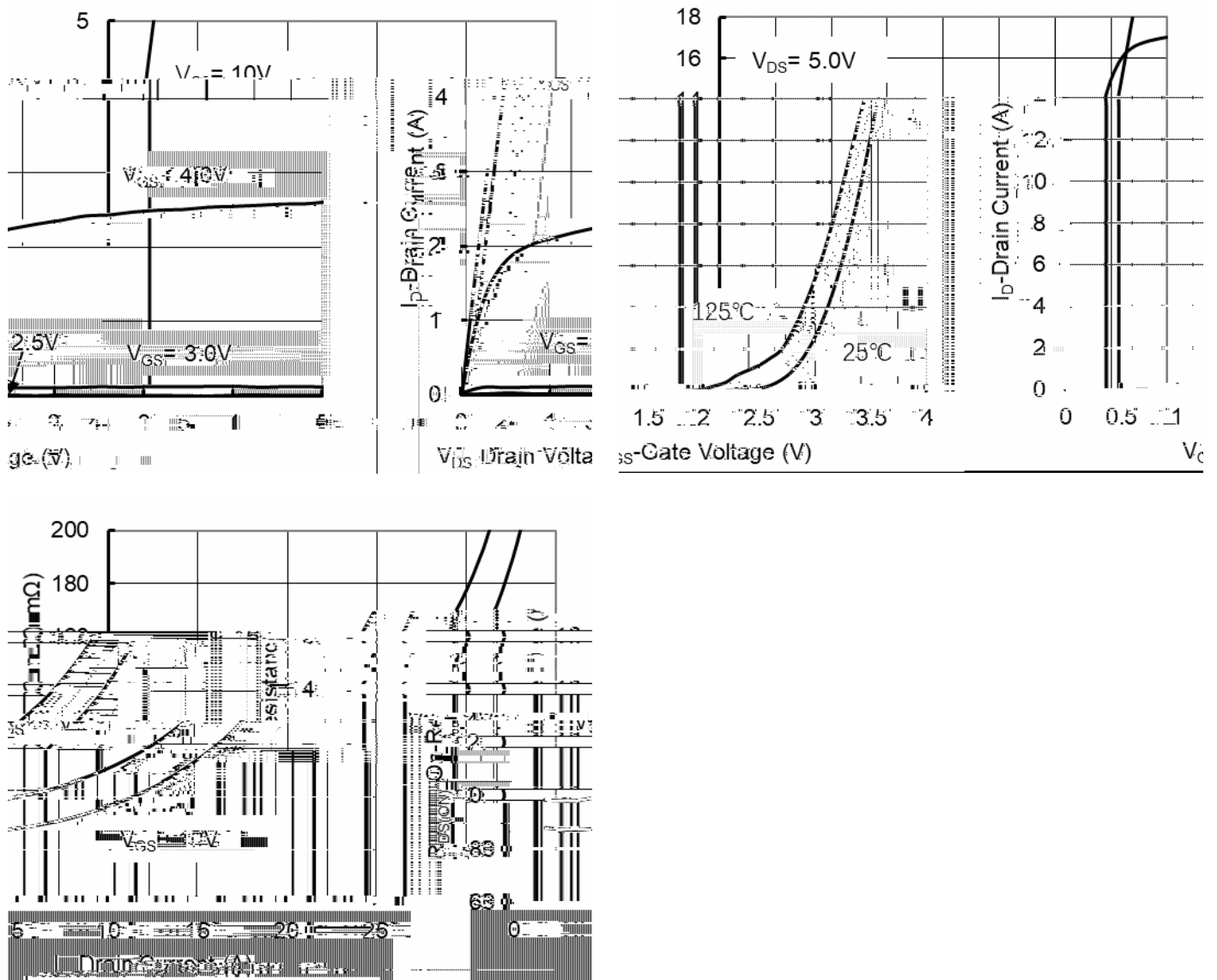


Ω (T_J=25 unless otherwise noted)

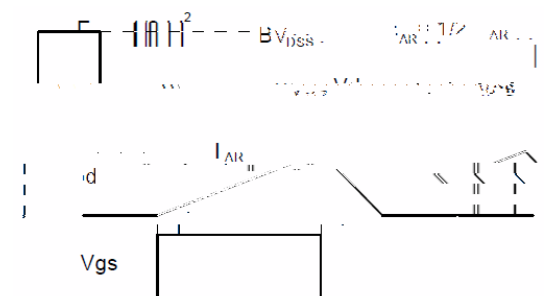
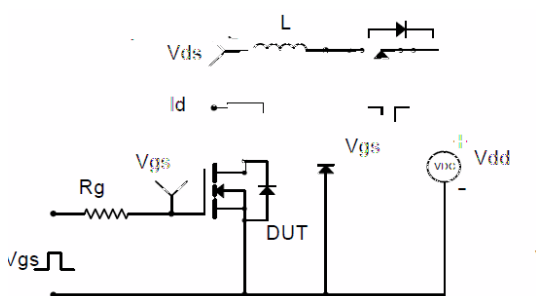
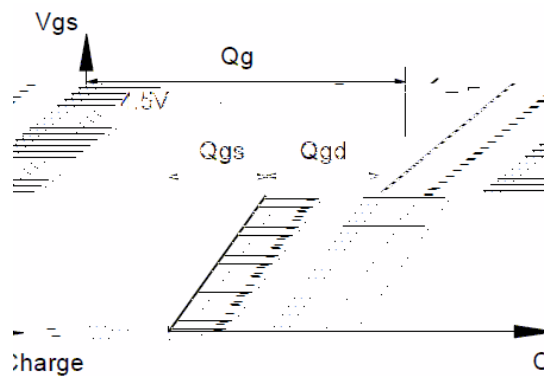
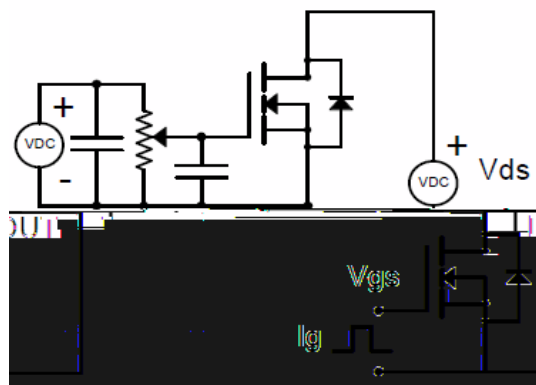
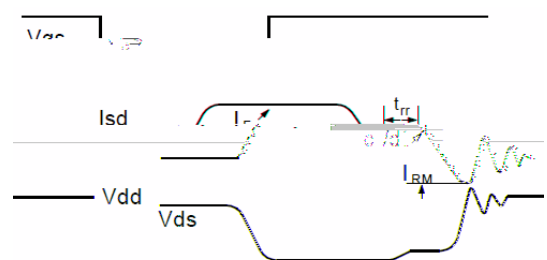
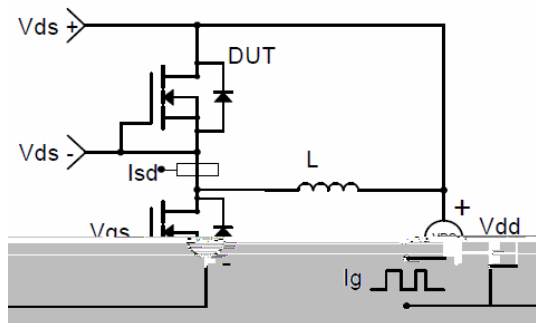
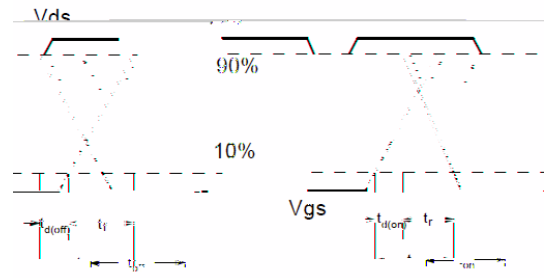
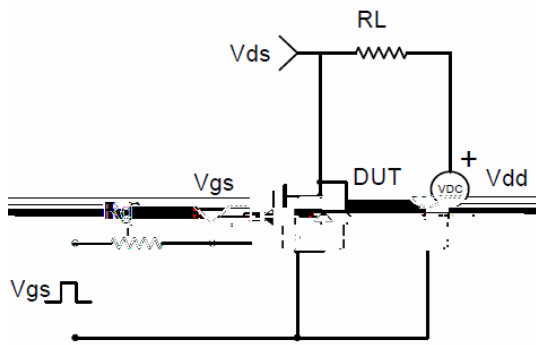
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250mA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	mA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = 020V, V _{DS} =0V			0100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250mA	1.1	1.8	3.0	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D =4A		95	110	mΩ
		V _{GS} = 4.5V, I _D =3.2A		100	120	
Diode Forward Voltage	V _{SD}	I _S =4A, V _{GS} =0V		0.8	1.2	V
Maximum Body-Diode Continuous Current	I _S				4	A
Input Capacitance	C _{ISS}	V _{DS} =50V, V _{GS} =0V, f=1MHZ		1070		pF
Output Capacitance	C _{OSS}			33		
Reverse Transfer Capacitance	C _{rss}			30		
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =50V, I _D =10A		26		nC
Gate-Source Charge	Q _{gs}			5.4		
Gate-Drain Charge	Q _{gd}			5.8		
Reverse Recovery Charge	Q _{rr}	I _F =10A, di/dt=100A/us		30.1		ns
Reverse Recovery Time	t _{rr}			40		
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =50V, R _L =6.4Ω R _{GEN} =3Ω		7		
Turn-on Rise Time	t _r			24		
Turn-off Delay Time	t _{D(off)}			24		
Turn-off fall Time	t _f			31		

A. Pulse Test: Pulse Width ≤ 300us, Duty cycle ≤ 2%.

B. R_{θJA} is the sum of the junction-to-lead and lead-to-ambient thermal resistance, where the lead thermal reference is defined as the solder mounting surface of the drain pins. R_{θJL} is guaranteed by design, while R_{θJA} is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.



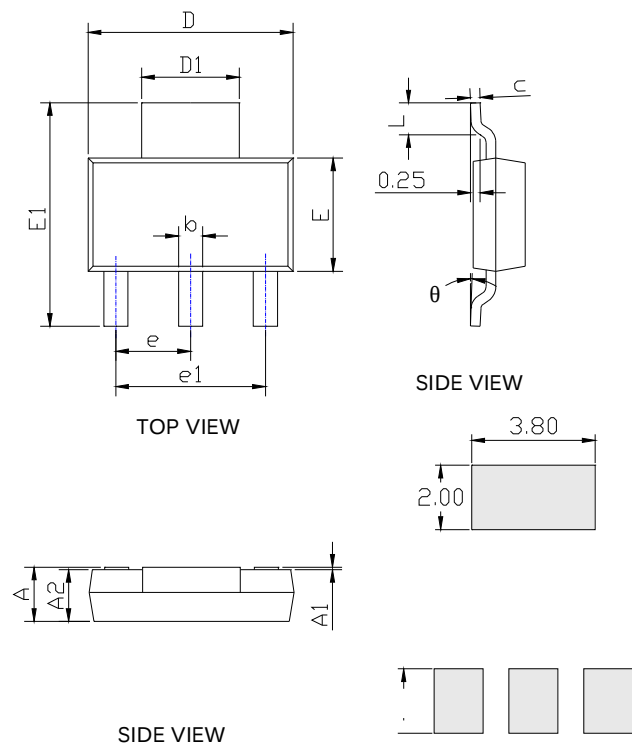
)LJXUH 1RUPD 0L7H 0QMLH 0W 7KHUPDO ,PSHGDQFH





Ω

TYPE B:



SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.060	0.071	1.520	1.800
A1	0.000	0.004	0.000	0.100
A2	0.059	0.067	1.500	1.700
b	0.026	0.032	0.660	0.820
c	0.010	0.014	0.250	0.350
D	0.244	0.252	6.200	6.400
D1	0.114	0.122	2.900	3.100
E	0.130	0.146	3.300	3.700
E1	0.269	0.278	6.830	7.070
e	0.091BSC		2.300BSC	
e1	0.177	0.185	4.500	4.700
L	0.035	0.045	0.900	1.150
θ	0°	10°	0°	10°



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